

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Diane L. Goss

Applicant : Shougo Imada, et al.
Application No. : 10/769,488
Filed : January 30, 2004
Title : MICROCOMPUTER LOGIC DEVELOPMENT

Grp./Div. : 2123
Examiner : Dwain M. Craig

Docket No. : 51879/A400

Confirmation No. 8744

**INFORMATION DISCLOSURE STATEMENT WITH FEE
UNDER 37 CFR §§ 1.97(c) AND 1.17(p)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
August 12, 2008

Commissioner:

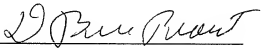
In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing the references that are known to applicant. A copy of each of the listed references is enclosed.

It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

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Enclosed is the processing fee of \$180 as required by 37 CFR § 1.17(p). The Commissioner is hereby authorized to charge any fees which may be required by this paper to Deposit Account No. 03-1728. Please show our docket number with any Deposit Account transaction.

Respectfully submitted,
CHRISTIE, PARKER & HALE, LLP

By 
D. Bruce Prout
Reg. No. 20,958
626/795-9900

DBP/dg
Enclosures: Copy of IDS
Form PTO/SB/08A/B, w/references

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